

Figures

Figure 1: A schematic diagram of a system architecture. The diagram shows a central processing unit (CPU) connected to a memory unit (RAM) and a storage unit (HDD). The CPU is also connected to a network interface (NIC) and a display unit (MONITOR). The storage unit is connected to the CPU and the network interface. The network interface is connected to a network (INTERNET). The display unit is connected to the CPU. The diagram is labeled with various components and their connections.

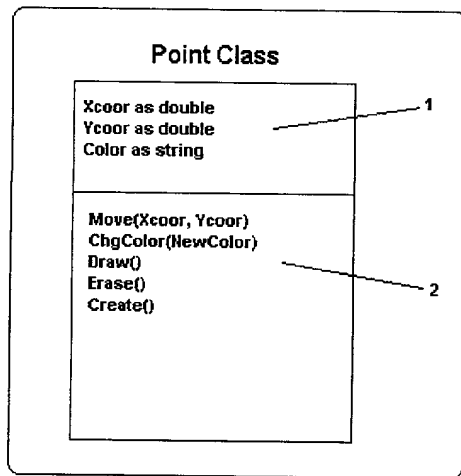


Fig. 1

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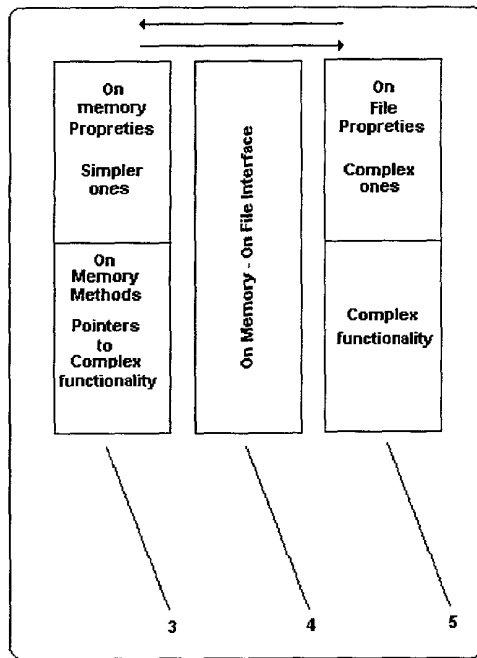


FIG. 2

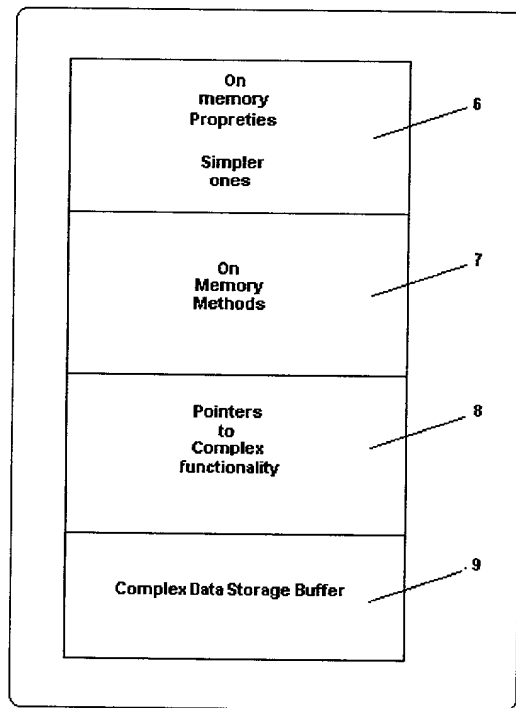


Fig. 3

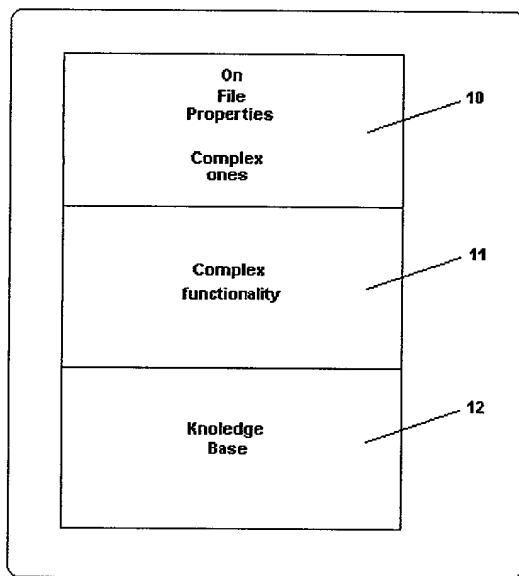


Fig. 3

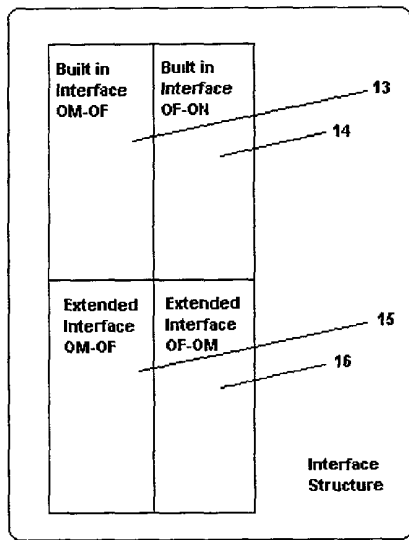


Fig. 5